Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1243	370/395.1	USPAT	OR	ON	2004/11/07 12:05
S2	82	Router same switch same fabric same interface	USPAT	OR	ON	2004/11/07 12:10
S3	20	Router same switch same fabric same interface and line adj card	USPAT	OR	ON	2004/11/07 12:13
S4	0	Router same switch same fabric same interface and backplane same bus same line adj card	USPAT	OR	ON	2004/11/07 12:13
S5	2	Router same switch same fabric same interface and backplane same line adj card	USPAT	OR	ON	2004/11/07 12:13
S6	50	Router same switch adj fabric same interface	USPAT	OR	ON	2004/11/07 12:17
S7	0.	Router same switch adj fabric same transmitter same receiver same data adj channel same interface	USPAT	OR	ON	2004/11/07 12:17
<b>S</b> 8	2	Router same switch adj fabric same transmitter same receiver same interface	USPAT	OR	ON	2004/11/07 12:18
S9	50	Router same switch adj fabric same interface	USPAT	OR	ON	2004/11/07 12:18
S10	4	S1 and S9	USPAT	OR	ON	2004/11/07 12:20
S11	5357	370/389	USPAT	OR -	ON	2004/11/07 12:20
S12	602	S11 and S1	USPAT	OR	ON	2004/11/07 12:20
S13	2	S12 and S6	USPAT	OR	ON	2004/11/07 12:22
S14	2538	370/412	USPAT	OR	ON	2004/11/07 12:35
S15	13	S14 and S6	USPAT	OR	ON	2004/11/07 12:37
<b>\$16</b>	6609	FPGA	USPAT	OR	ON	2004/11/07 12:38
S17	39	FPGA and router and switch adj fabric	USPAT	OR	ON	2004/11/07 12:53
S18	26	FPGA and router and switch adj fabric and cell	USPAT	OR	ON	2004/11/07 12:42
S19	1078	370/413	USPAT	OR	ON	2004/11/07 12:43
S20	3	S19 and S17	USPAT	OR	ON	2004/11/07 12:43
S21	518	370/415	USPAT	OR	ON	2004/11/07 12:45
S22	1	S17 and S21	USPAT	OR	ON	2004/11/07 12:46
S23	1546	370/428	USPAT	OR	ON	2004/11/07 12:46
S24	0	"l28" and S17	USPAT	OR	ON	2004/11/07 12:46
S25	2	S23 and S17	USPAT	OR	ON	2004/11/07 12:47
S26	1006	370/429	USPAT	OR	ON	2004/11/07 12:47
S27	3	S26 and S17	USPAT	OR	ON	2004/11/07 12:50

S28	588	710/102	USPAT	OR	ON	2004/11/07 12:52
S29	0	S28 and S17	USPAT	OR	ON	2004/11/07 12:50
S30	407	710/106	USPAT	OR	ON	2004/11/07 12:52
S31	442218	FPGA and router and switch adj fabric and I/O interface	USPAT	OR	ON	2004/11/07 12:53
S32	2	FPGA and router and switch adj fabric and I/O adj interface	USPAT	OR	ON	2004/11/07 12:54